and so on are performed with respect to the raw data input to the first white balance adjusting circuit 62 therein, the raw data is stored in the SDRAM 17, and read from the SDRAM 17. And, using the gain and the like calculated in the first white balance adjusting circuit 62, the white balance adjustment is performed on the raw data in the second white balance adjusting circuit 32. Therefore, from when the raw data is input to the first white balance adjusting circuit 62 until it is output from the second white balance adjusting circuit 32, it is possible to calculate values set for white balance for the raw data, and white balance adjustment can be performed on the input raw data using the information thereof. Therefore, high-quality image with satisfactory white balance precision can be obtained.

In the white balance adjustment, the gain adjustment is desirably performed on raw data using the information thereof. In the above mentioned still image mode, a white balance gain for raw data is calculated using the light source information extracted from the raw data for one screen, and the white balance adjustment can be performed on the retrieved raw data for one screen using such white balance gain.

In the camera which captures a still image such as a digital camera, the white balance adjustment is desirably